

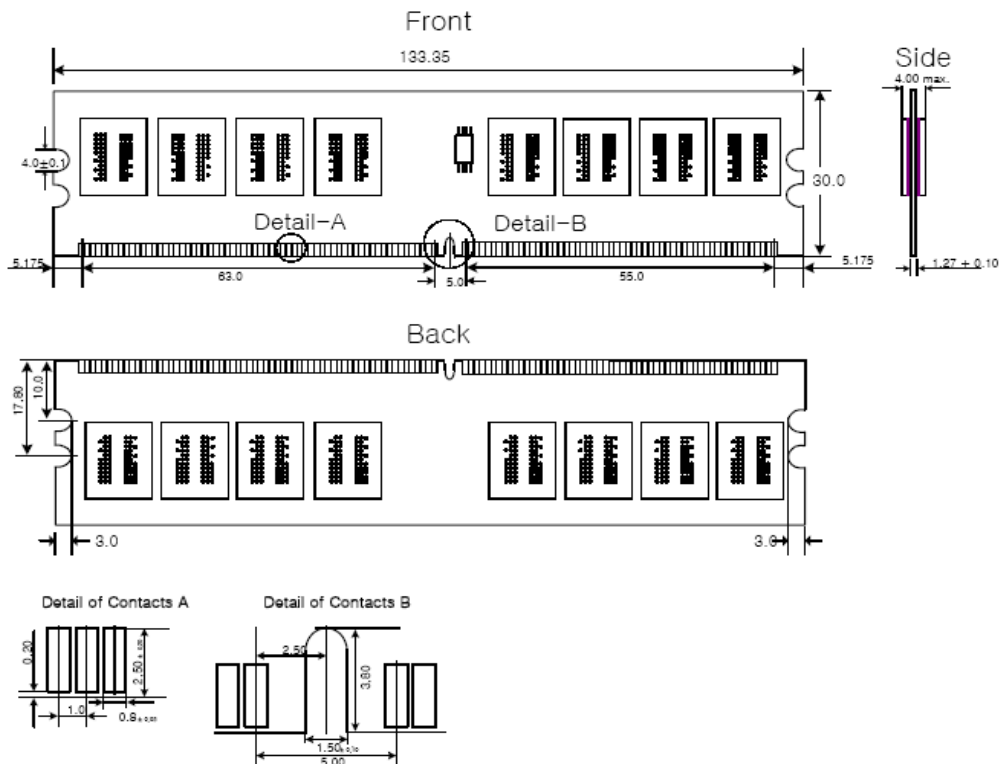
Memory Module Data Sheet L126423F

1GB 128M x 64-bit DDR2 667 (PC2-5300)

SPECIFICATIONS;

- 1GB (128M x 64-bit) Unbuffered DDR2 667Mhz DIMM
- 60-ball FBGA 64M x 8 (16M x 8 bit x 4 Bank/200Mhz) x Sixteen DRAM
- 240-pin Dual in-line memory module with Gold Plated Contacts
- Dimm Dimensions 133.35 x 30 x 4 (mm)
- Programmable CAS Latency Supported - CL 5
- Row Cycle Time (tRC) – 60ns
- Write Recovery Time (tWR) - 15ns
- Row Active Time (tRAS) – 45ns
- Clock Cycle Time (tCK) – 3000ps (min)
- Power Supply Voltage – 1.8V (+/- .01V)
- Operating Temperature (TA) - 0~55 Celsius
- Module Timing; 5 – 5 – 5

PACKAGE DIMENSIONS



NOTE: The module described in this data sheet is one of several configurations available under this part number. All configurations are compatible but the DRAM combination and the module height may vary from what is described. However, in no configurations will the module height exceed 31.75mm.