

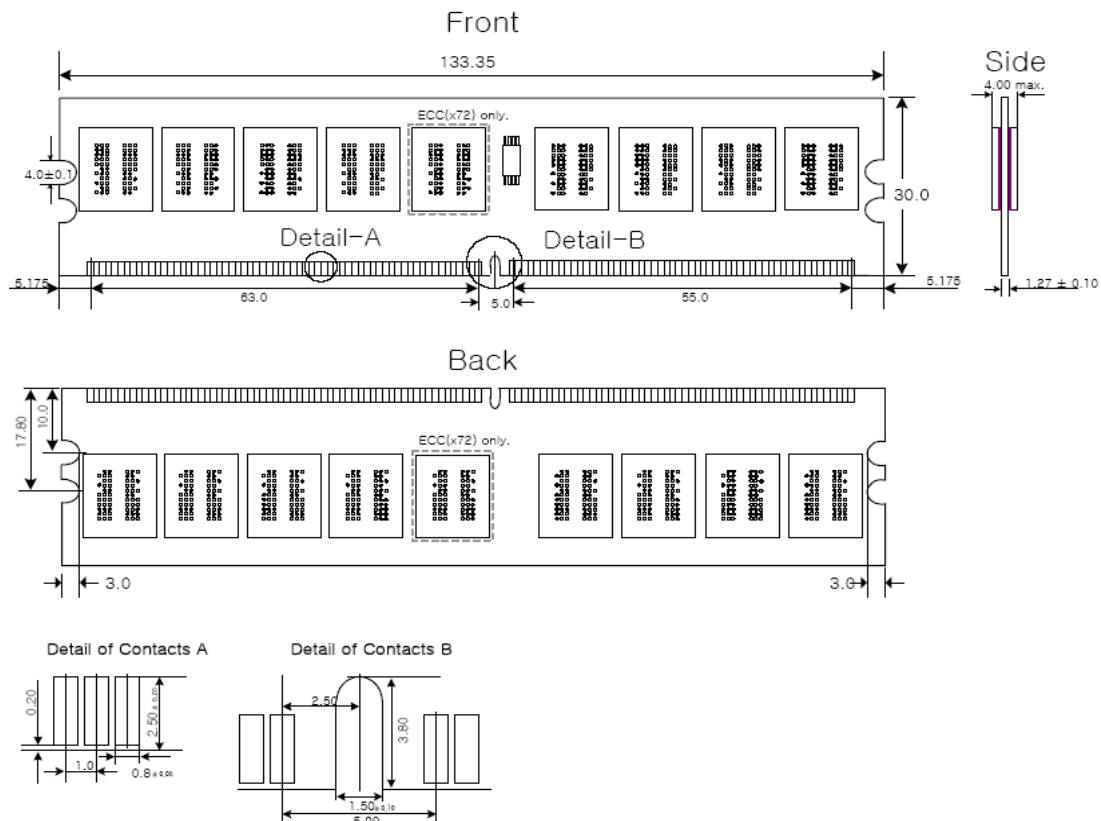
Memory Module Data Sheet L127223E

1GB 128M x 72-bit DDR2 533 (PC4300)

SPECIFICATIONS;

- 1GB (64M x 72-bit) Unbuffered DDR2 533 DIMM
- Error Check Correction (ECC) Capability
- 84-ball FBGA 64M x 8 (16M x 8 bit x 4 Bank/200Mhz) x Eighteen DRAM
- 240-pin Dual in-line memory module with Gold Plated Contacts
- Dimm Dimensions 133.35 x 30 x 4 (mm)
- Programmable CAS Latency Supported - CL 4
- Row Cycle Time (tRC) – 60ns
- Auto Refresh Cycle Time (tRFC) - 105 ns
- Row Active Time (tRAS) – 45ns
- Clock Cycle Time (tCK) – 3750ps (min)
- Power Supply Voltage – 1.8V (+/- .01V)
- Operating Temperature (TA) - 0~55 Celsius

PACKAGE DIMENSIONS



NOTE: The module described in this data sheet is one of several configurations available under this part number. All configurations are compatible but the DRAM combination and the module height may vary from what is described. However, in no configurations will the module height exceed 31.75mm.