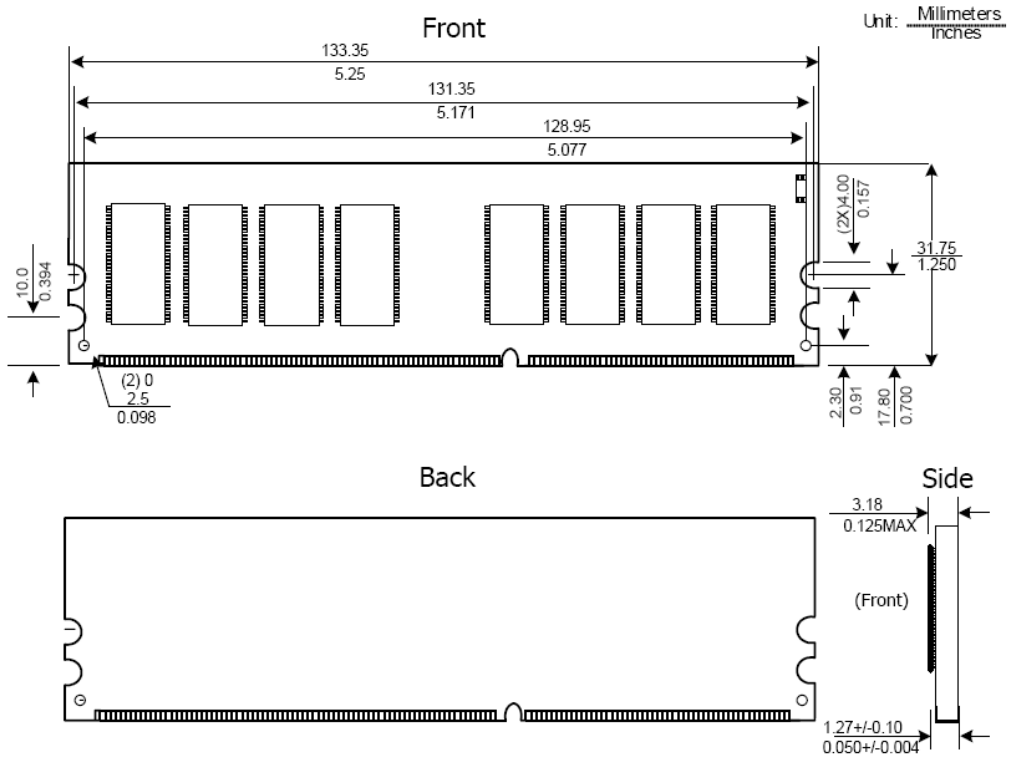
Memory Module Data Sheet
H6464D47

512MB 64M x 64-bit **DDR400** (PC3200)

SPECIFICATIONS;

- 512MB (64M x 64-bit) Unbuffered DDR400 DIMM
- TSOP-II 64M x 8 (16M x 8 bit x 4 Bank/200Mhz) x Eight DRAM
- JEDEC Standard Double Data Rate Synchronous DRAM's (DDR SDRAM)
- 184-pin Dual in-line memory module with Gold Plated Contacts
- Dimm Dimensions 133.35 x 31.75 x 4 (mm)
- Programmable CAS Latency Supported - CL 3, 2.5, 2
- Row Cycle Time (tRC) - 55ns
- Auto Refresh Cycle Time (tRFC)- 70ns
- Row Active Time (tRAS) - 40ns
- Clock Cycle Time (tCK) - 5ns
- Power Supply Voltage - 2.5V (+/- .01V)
- Operating Temperature (TA) - 0~70 Celsius
- Module Timing 3-3-3

PACKAGE DIMENSIONS



NOTE: The module described in this data sheet is one of several configurations available under this part number. All configurations are compatible but the DRAM combination and the module height may vary from what is described. However, in no configurations will the module height exceed 31.75mm.