

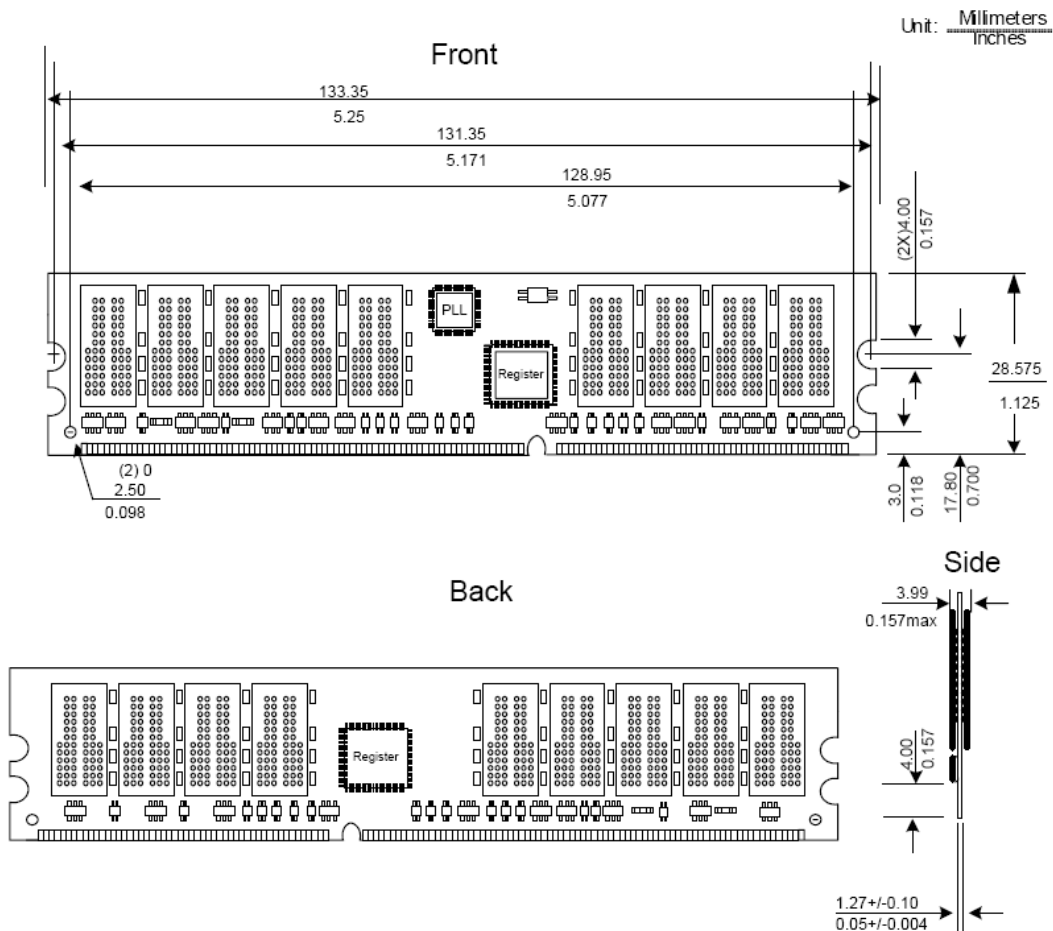
Memory Module Data Sheet L6472Y37

512MB 64M x 72-bit ECC Registered DDR400 (PC3200)

SPECIFICATIONS;

- 512MB (64M x 72-bit) Registered DDR400 DIMM
- Error Check Correction (ECC) Capability
- BGA 32M x 8 (8M x 8 bit x 4 Bank/200Mhz) x Eighteen DRAM
- 184-pin Dual in-line memory module with Gold Plated Contacts
- Dimm Dimensions 133.35 x 28.575 x 3.99 (mm)
- Programmable CAS Latency Supported - CL 3, 2.5, 2
- Row Cycle Time (tRC) - 55ns
- Auto Refresh Cycle Time (tRFC)- 70ns (min.)
- Row Active Time (tRAS) - 40ns
- Clock Cycle Time (tCK) - 5ns
- Power Supply Voltage - 2.5V (+/- .01V)
- Operating Temperature (TA) - 0~70 Celsius

PACKAGE DIMENSIONS



NOTE: The module described in this data sheet is one of several configurations available under this part number. All configurations are compatible but the DRAM combination and the module height may vary from what is described. However, in no configurations will the module height exceed 31.75mm.